Appl. No. 10/624,454 Amdt. dated December 20, 2007 Reply to Office Action of October 2, 2007

## REMARKS/ARGUMENTS

Claims 1-46 are currently pending in the application. Claims 1, 4, 7-9, 13, 22-24, 27, 35, and 36 have been amended. No claims have been added or cancelled. Therefore, claims 1-46 are present for examination.

## Claim Rejection Under 35 U.S.C. § 102

Claims 1-8, 12, 17, 21-25, 27, 32, 35-37, 39 and 44 have been rejected under 35 U.S.C. 102(e) as being anticipated by U.S. Patent No. 7,035,270 to Moore, Jr., et al. ("Moore").

Applicants submit that Moore discloses "an interface between an HFC network and an in-home network." (See Moore at Abstract.) Moore further discloses that the interface includes "[a] processor, a non-volatile memory and RAM." (See col. 4, lines 8-9 and Fig. 2.) Even though Moore discloses that the interface includes a processor Moore merely discloses that the processor has the "ability to convert the in-home networking protocol-related information to HFC network parameters." (See Moore at col. 5, line 17-19.) The Office Action at page 3, line 10-14 asserts that Moore's processor performs the same functions as claim 1's and similarly claims 22 and 35's microserver. Applicants respectfully disagree with the Office Action's assertion.

In contrast, claim 1 and similarly claims 22 and 35 recite "a microserver...[which] includes software for implementing at least one of an authentication microserver, a file-transfer microserver, a dynamic host configuration protocol microserver, or webserver microserver to function a predetermined function over the internal transport medium by processing the received telecommunication information." Applicants submit that Moore's processor does not perform the same functions as claim 1's microserver. Specifically, Moore's processor converts in-home networking protocols to HFC network parameters, and fails to implement the functionality of "an authentication microserver, a file-transfer microserver, a dynamic host configuration protocol microserver, or webserver microserver", as recited by claim 1. Stated differently, Moore's processor is a protocol translator while claim 1's microserver has the functionality of a variety of

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servers. Therefore, for at least this reason, Applicants submit that claim 1 and similarly claim 22 and 35 are patentable over Moore.

Furthermore, claims 2-21, 23-34 and 36-46 depend from independent claims 1, 22, and 35, respectively. Thus, by virtue of their dependence on an allowable independent claim, Applicants submit that claims 2-21, 23-34 and 36-46 are also allowable. Accordingly, Applicants respectfully request that this rejection be withdrawn.

## Claim Rejection Under 35 U.S.C. § 103

Claims 9-11, 13-16, 18-20, 26, 28, 30, 31, 33-34, 38, 40, 42, 43, and 45-46 have been rejected under 35 U.S.C. 103(a) as being unpatentable over the Moore patent in view of U.S. Patent No. 6,970,127 to Rakib ("**Rakib**").

Claims 14, 29 and 41 have been rejected under 35 U.S.C. 103(a) as being unpatentable over the Moore patent in view of U.S. Patent No. 5,694,616 to Johnson, et al. ("Johnson").

Claims 9-11, 13-16, 18-20, 26, 28-31, 33-34, 38, 40-43, and 45-46 depend from one of independent claims 1, 22, and 35. Thus, by virtue of their dependence on an allowable independent claim, Applicants submit that claims 9-11, 13-16, 18-20, 26, 28-31, 33-34, 38, 40-43, and 45-46 are also allowable. Accordingly, Applicants respectfully request that these rejection be withdrawn.

## **CONCLUSION**

In view of the foregoing, Applicants believe all claims now pending in this Application are in condition for allowance. The issuance of a formal Notice of Allowance at an early date is respectfully requested.

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 303-571-4000.

Respectfully submitted,

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